4th Generation Intel® Core™ Processor-based Platforms with Intel® Communications Chipset 89xxCC Series

A range of Intel® processors, paired with the Intel® Communications Chipset 89xxCC series, provide a scalable platform utilizing two- and four-core systems for single-socket designs.

Product Overview

Manufactured on industry-leading 22nm process technology with 3D Tri-Gate transistors, these 4th generation Intel® Core™ processors are based on the Intel® microarchitecture, codename Haswell, offering numerous advancements over previous processors (codename Ivy Gladden). The introduction of Intel® Advanced Vector Extensions (Intel® AVX) 2.0 benefits math, codec, image, and digital signal processing software while improving performance in face, detection, professional imaging, and high-performance computing.

Paired with the Intel® Communications Chipset 89xxCC series, this processor-based platform extends the scalability of Intel® architecture (IA) into smaller footprint communications systems without sacrificing industry requirements such as workload acceleration and compression, multicore processing, and virtualization.

The platform addresses the needs of thermally constrained solutions, such as midrange security appliances, branch office routers, network access, media and communications servers, small cells, control plane processing, and storage.

By scaling the powerful IA instruction set into lower power network nodes, the platform enables software reuse, power management, and software-based load balancing. The Intel® Data Plane Development Kit (Intel® DPDK) complements the platform by improving packet processing speeds to handle increasing network traffic data rates and associated control/signaling requirements.

With thermal design power (TDP) options of 27 W and 43 W, these processors are designed for single processor systems of two or four cores. They are pin-to-pin compatible and deliver a consistent feature set, allowing developers to design a footprint and later choose a specific processor, based on performance-per-watt requirements.

Processors include Intel® Hyper-Threading Technology1 to boost performance for multi-threaded applications, while Intel® Virtualization Technology2 (Intel® VT-d and Intel® VT-x) increases virtualization performance and robustness by allowing the operating system more direct access to the hardware.

With up to 16 PCI Express* 3.0 lanes, these processors provide multiple high-speed component interfaces. Up to 8 MB Intel® Smart Cache (L3 Cache) supports complex pipelines and large control plane tables. An integrated memory controller supports DDR3/DDR3L protocols with up to two channels (up to two DIMMs per channel), runs at speeds up to 1600 MT/s, and features optional Error Correcting Code (ECC) memory. This enables product designers to make memory decisions based on specific features, performance, and budget parameters.
The Intel Communications Chipset 89xxCC series includes Intel® QuickAssist Technology with improved cryptographic and compression performance. This provides hardware acceleration to assist with the performance demands of securing and routing Internet traffic and other workloads such as compression and wireless 3G/4G LTE algorithm offload, thereby reserving processor cycles for application processing. (See a full list of cryptographic and compression functions on page 3.)

This chipset series provides hardware offload assistance (up to 20 Gbps for virtual private networks [VPNs]) and helps storage and network optimization applications better handle compression and decompression tasks. Kasumi and Snow 3G functions within Intel QuickAssist Technology help with 3G and 4G LTE mobile gateway and infrastructure workloads. Four integrated GbE MACs save board real estate and BOM costs over previous-generation Intel chipsets.

These processors and chipsets are manufactured with lead-free¹ and halogen-free⁴ component packages and provide at least seven-year availability and ten-year reliability support. From modular components to market-ready systems, Intel and the 250+ global member companies of the Intel® Intelligent Systems Alliance (intel.com/go/intelligentsystems-alliance) provide the performance, connectivity, manageability, and security developers need to create smart, connected systems.

Figure 1. Two- and four-core Intel® processors for single-socket designs, paired with the Intel® Communications Chipset 89xxCC series, make this an ideal platform for smaller footprint communications infrastructure systems.
# Intel® Core™ Processors for Smaller Footprint Communications Infrastructure Systems

## Features | Benefits
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**Error Correcting Code (ECC) (optional)** |  • Enhances performance, uptime, and autonomous operation.
  • Detects multiple-bit memory errors and locates/corrects single-bit errors to keep a system up and running without requiring system reset.

**Intel® Hyper-Threading Technology**<sup>1</sup> |  • Additional logical cores on Intel® architecture-based systems increase control plane and data plane performance. Boosts performance for parallel, multi-threaded applications.

**PCI Express® Gen 3.0** |  • Fast access to peripheral and networking devices: up to 16 PCI Express lanes with three PCI Express controllers that can be independently configured to Gen 1.0, Gen 2.0, or Gen 3.0, allowing operation at 2.5 Giga-transfers/second, 5.0 Giga-transfers, and 8 Giga-transfers data rates.

**Intel® Virtualization Technology**<sup>2</sup> for IA-32 and Intel® 64 (Intel® VT-x) |  • Faster performance for core virtualization processes.
  • Improves application performance, live migration, provisioning, dynamic load balancing, and disaster recovery.

**Intel Virtualization Technology for Directed I/O (Intel® VT-d)** |  • Built-in hardware support for I/O virtualization.
  • Improves I/O performance, increases system reliability, and provides enhanced memory protection.

**Intel® Advanced Encryption Standard New Instructions (Intel® AES-NI)**<sup>5</sup> |  • Processor assistance for accelerating AES encryption and decryption used in multiple communications workloads.

**Enhanced Intel SpeedStep® Technology**<sup>6</sup> |  • Multiple frequency and voltage points for optimal performance and power efficiency.

**Intel® Advanced Vector Extensions 2.0** |  • Supports faster performance on digital signal and image processing workloads of compute-intensive applications.

**Fully Integrated Voltage Regulator** |  • Simplifies power delivery by integrating legacy power delivery onto processor package/die.

**Intel® Transactional Synchronization Extensions - New Instructions (Intel® TSX-NI)** |  • Allows programmers to specify regions of code for transactional synchronization. These extensions can be used to achieve the performance of fine-grain locking while actually programming with coarse-grain locks.
  Details on Intel TSX-NI may be found in Intel® Architecture Instruction Set Extensions Programming Reference.

## Intel® Communications Chipset 89xxCC Series

## Features | Benefits
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**PCI Express® Interface** |  • Up to 2.5 Giga-transfers/s for fast access to peripheral devices and networking with up to four PCI Express® lanes; configurable as 4x1, 2x2, 1x2 + 2x1 or 1x4.

**Intel® QuickAssist Technology** |  • Delivers hardware acceleration services for efficient cryptographic and compression performance.
  • Symmetric cryptography functions include: cipher operations (AES, DES, 3DES, ARC4); wireless (Kasumi, Snow3G); hash/authenticate operations (SHA-1, MDS, SHA-2 [SHA-224, SHA-256, SHA-384, SHA-512]); authentication (HMAC, AES-XCBC, AES-CCM); random number generation.
  • Public key functions include: RSA operation; Diffie-Hellman operation; digital signature standard operation; key derivation operation; elliptic curve cryptography (ECDSA and ECDH); random number generation and prime number testing.
  • Compression/decompression include: DEFLATE (Lempel-Ziv 77); LZS (Lempel-Ziv-Stac).

**Intel® QuickAssist Technology Software Support** |  • Provides support for Fedora Linux®.

**Four Integrated GbE MACs** |  • Simplifies reuse; compatible with standard Intel® Ethernet drivers.

**SATA Gen 2 ports (3 Gb/s)** |  • Supports faster transfer rate for improved data access.

**Universal Serial Bus (USB) 2.0** |  • Greater performance enhancement with a design data rate of up to 480 Mbps.
  • Rate-matching hub enables lower power requirements and manages the transition of the communication data rate from the high speed of the host controller to the lower speed of USB full-speed/low-speed devices.

**Intel® Management Engine Ignition Firmware** |  • Supports essential platform functionality.

**Intel® Data Plane Development Kit** |  • Delivers breakthrough packet processing performance on Intel architecture.
## Intel® Core™ Processors for Smaller Footprint Communications Infrastructure Systems

<table>
<thead>
<tr>
<th>Product Name</th>
<th>Intel® Core™ i7-4700EC Processors</th>
<th>Intel® Core™ i7-4702EC Processors</th>
<th>Intel® Core™ i5-4402EC Processors</th>
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<td>43 W</td>
<td>27 W</td>
<td>27 W</td>
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<td>Cores/Threads</td>
<td>4/8</td>
<td>4/8</td>
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<td>Intel® Smart Cache (L3 Cache)</td>
<td>8 MB</td>
<td>8 MB</td>
<td>4 MB</td>
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<tr>
<td>Max Memory Size (dependent on memory type)</td>
<td>32 GB</td>
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<td>Memory Types</td>
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<td>DDR3/DDR3L 1333/1600</td>
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<td>Memory Channels</td>
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<td>Max Memory Bandwidth</td>
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## Intel® Communications Chipset 89xxCC Series

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<tr>
<td>Intel® QuickAssist Technology</td>
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<td>PCI Express Gen 1.0</td>
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<td>USB 2.0 ports</td>
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<td>Integrated Gigabit Ethernet MACs</td>
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<td>GPIO pins (multiplexed or dedicated)</td>
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<td>27 mm x 27 mm (0.7 mm variable pitch)</td>
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Intel in Communications: [intel.com/go/commsinfrastructure](http://intel.com/go/commsinfrastructure)

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*This Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. For more information, visit http://www.intel.com/products/processor_number.

*Requires an Intel® HT Technology-enabled system. Consult your PC manufacturer. Performance will vary depending on the specific hardware and software used. For more information, including details on which processors support HT Technology, visit http://www.intel.com/info/hyperthreading.

*Intel® Virtualization Technology requires a computer system with an enabled Intel® processor, BIOS, and virtual machine monitor (VMM). Functionality, performance, or other benefits will vary depending on hardware and software configurations. Software applications may not be compatible with all operating systems. Consult your PC manufacturer. For more information, visit http://www.intel.com/go/virtualization.

*This product is manufactured on a lead-free process. Lead is below 1000 PPM per EU RoHS directive (2002/95/EC, Annex A). No exemptions required.

*Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel® components, as well as purchased components on the finished assembly, meet J-709 requirements, and the PCB/Substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

*Intel® AES-NI requires a computer system with an AES-NI-enabled processor, as well as non-Intel software, to execute the instructions in the correct sequence. AES-NI is available on select Intel® processors. For availability, consult your retailer or system manufacturer. For more information, see the Intel® Advanced Encryption Standard Instructions (AES-NI). *See the Processor Spec Finder at http://ark.intel.com or contact your Intel representative for more information.

*WARNING:* Improper handling of solid state devices may cause permanent damage. There are no user-serviceable parts inside. Refer servicing to qualified personnel.

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