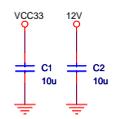
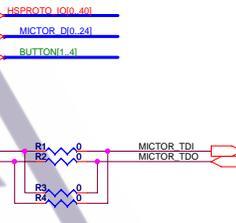


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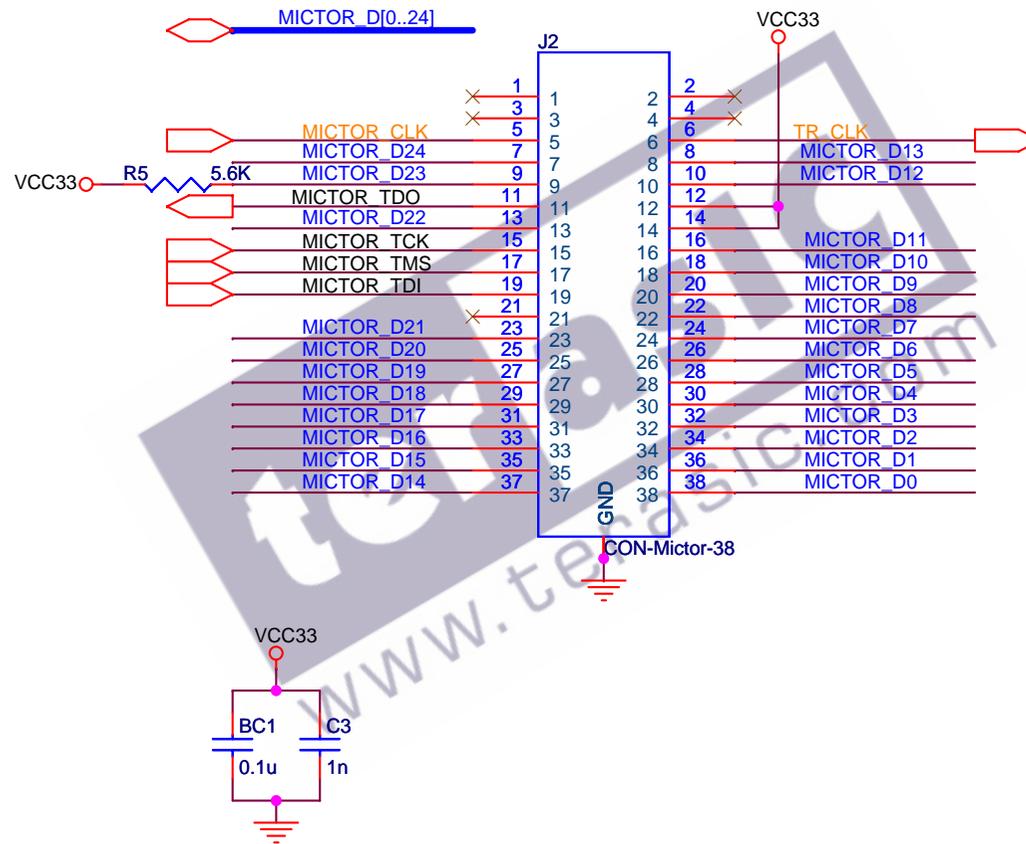
Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number Top Level Hierarchy	Rev 1.1
Date:	Thursday, August 23, 2007	Sheet 1 of 8



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File		HSMC to Santa Cruz Daughter Board	
Size	c	Document Number	High Speed Mezzanine Card Connector
Date:	Thursday, August 23, 2007	Sheet	2 of 8
		Rev	1.1

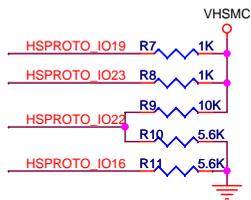
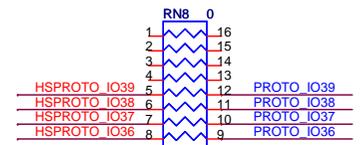
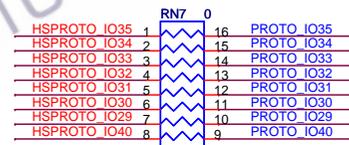
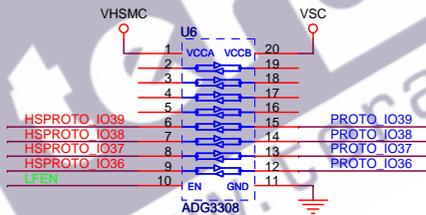
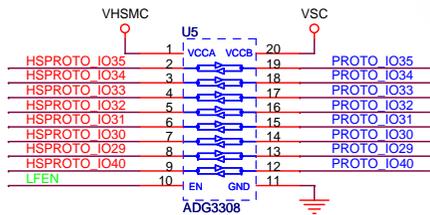
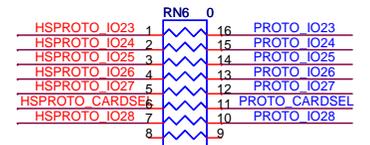
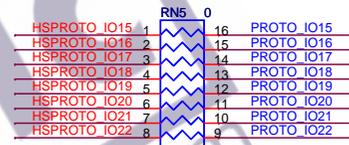
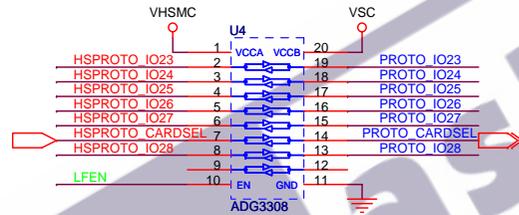
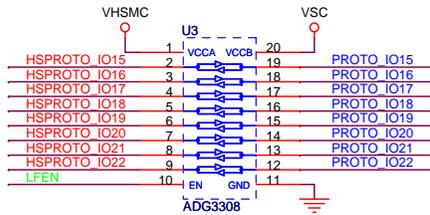
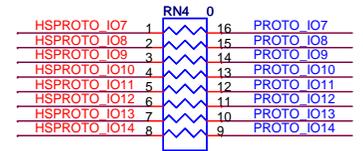
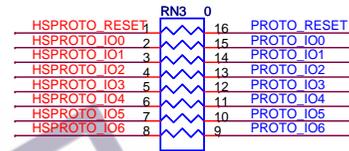
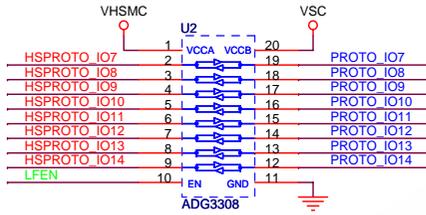
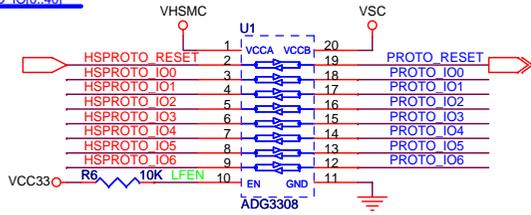


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Mictor Connector		
Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number Mictor Connector	Rev
Date:	Thursday, August 23, 2007	Sheet 3 of 8 1.1

HSPROTO_IO[0..40]
 PROTO_IO[0..40]

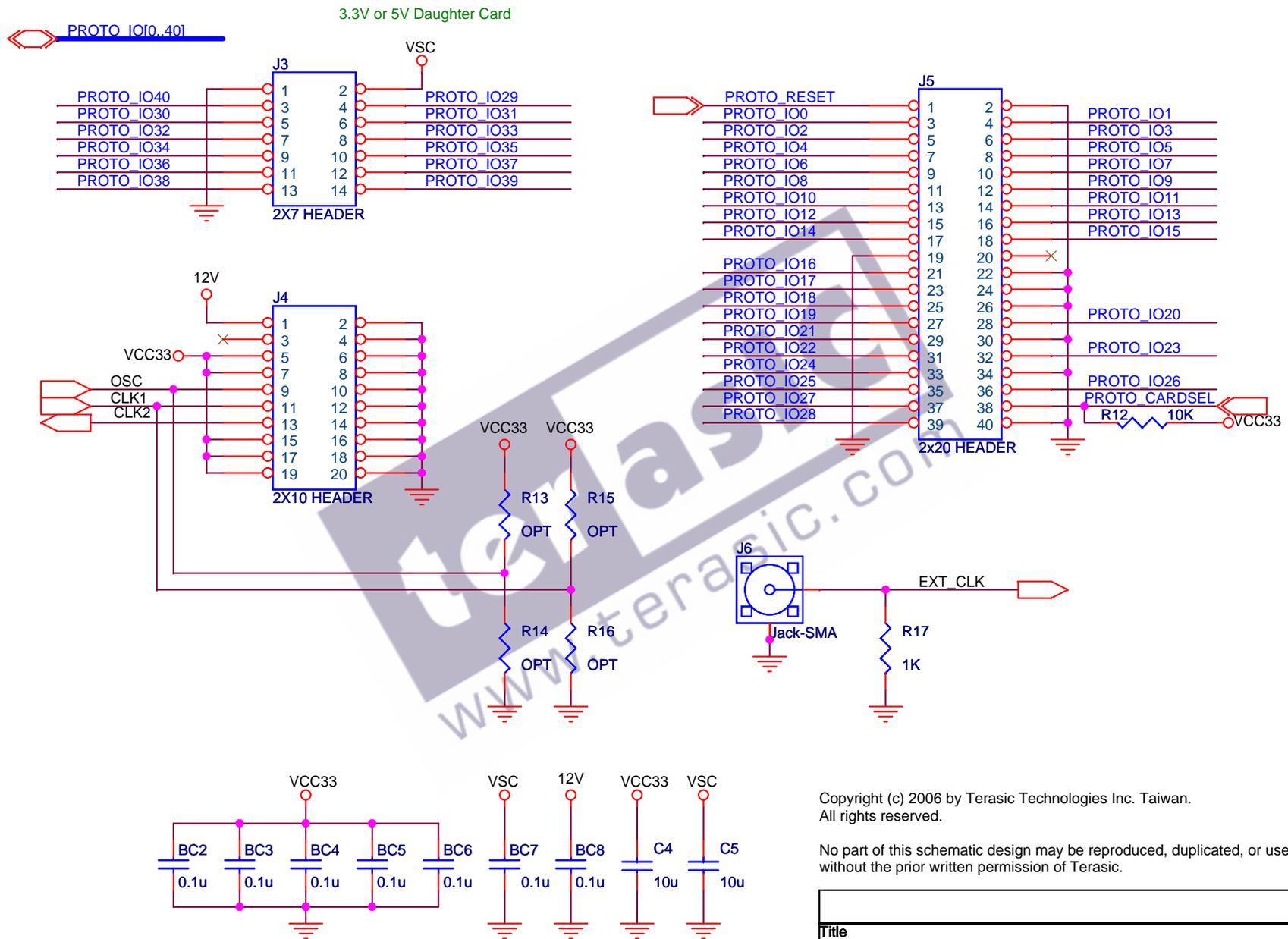


R9 and R10 are stuff options for the IDE determining whether IDE INTRQ gets a pull down or pull up. R9 should be a nostuff for production.

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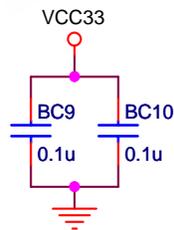
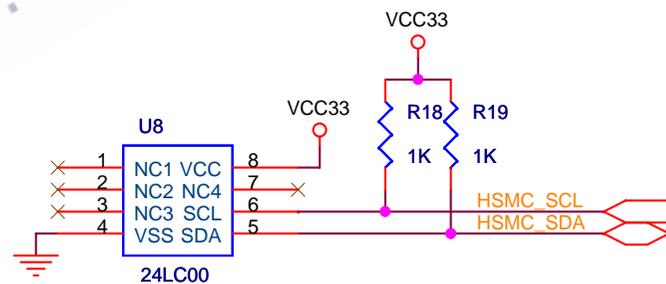
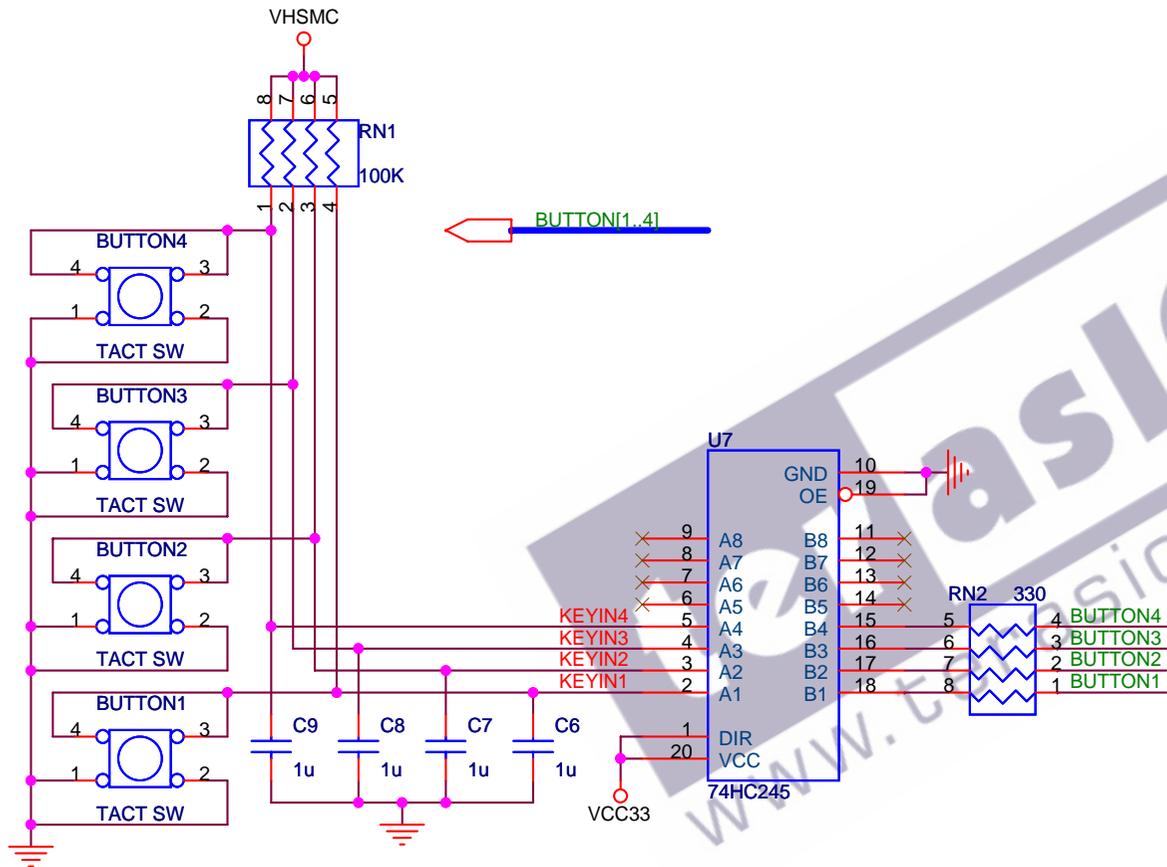
Title		
HSMC to Santa Cruz Daughter Board		
Size	Document Number	Rev
B	ADG3308 Level Shift and Bypass Resistor	1.1
Date:	Thursday, August 23, 2007	Sheet 4 of 8



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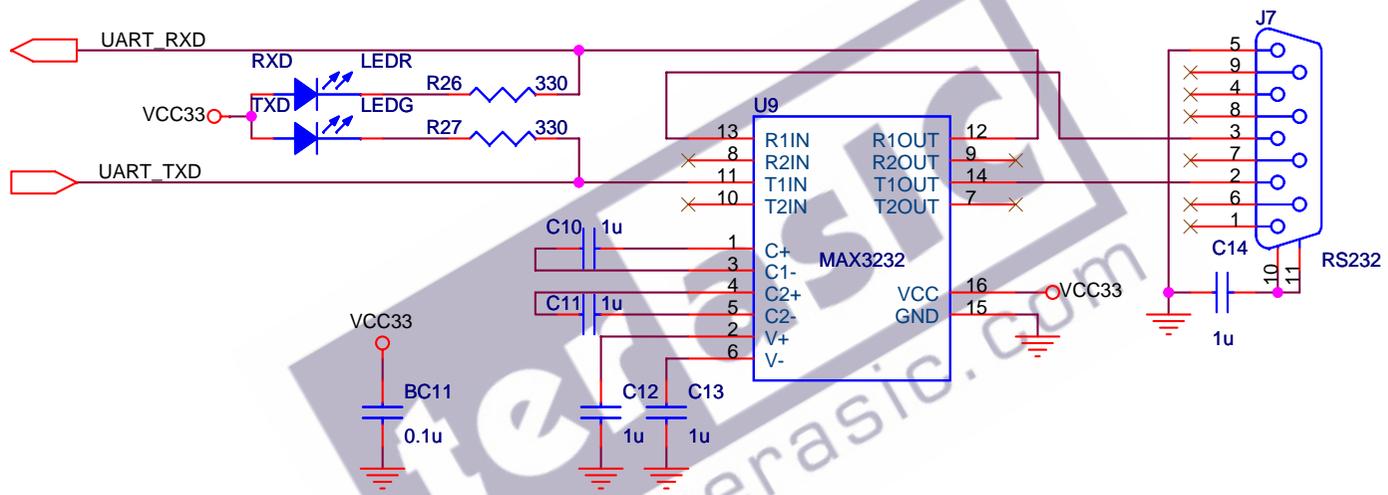
Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number Santa Cruz Connector	Rev 1.1
Date:	Thursday, August 23, 2007	Sheet 5 of 8



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Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number TACT Switch and EEPROM	Rev 1.1
Date:	Thursday, August 23, 2007	Sheet 6 of 8

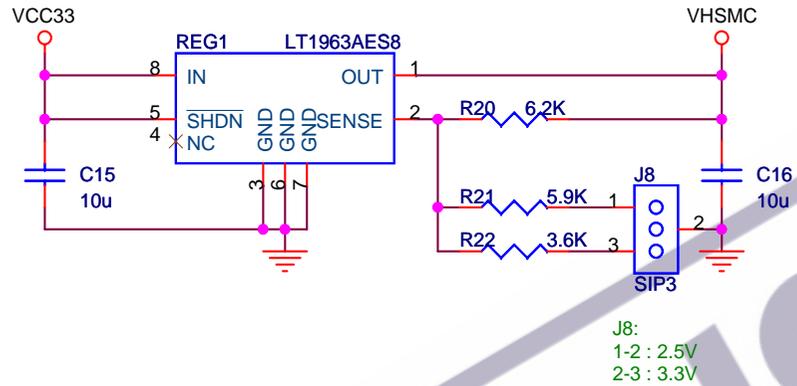


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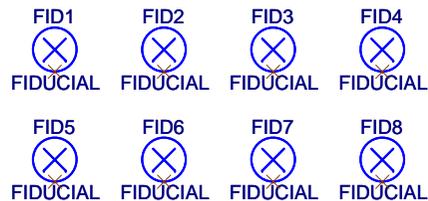
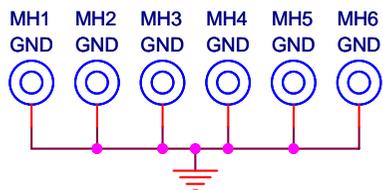
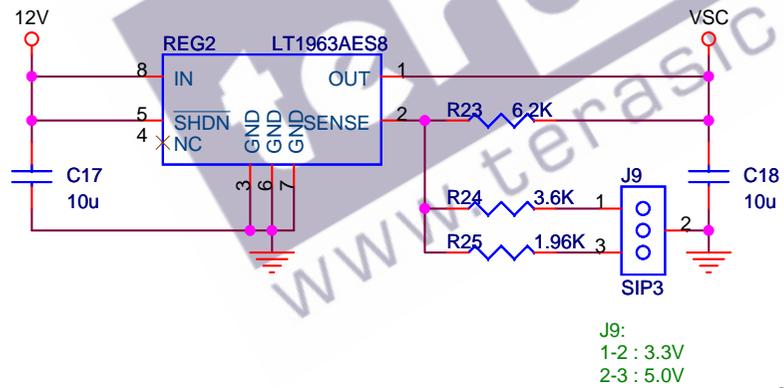
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Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number RS-232	Rev 1.1
Date:	Thursday, August 23, 2007	Sheet 7 of 8

2.5V or 3.3V for HSMC Source Voltage



3.3V or 5V for Santa Cruz Connector Level Shift



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Title		
HSMC to Santa Cruz Daughter Board		
Size A	Document Number LT1963AES8 LDO Regulator	Rev 1.1
Date:	Thursday, August 23, 2007	Sheet 8 of 8