



# High-speed Reed-Solomon IP Core Release Notes



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## 1 High-speed Reed-Solomon IP Core Release Notes

If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

### Related Links

[Intel Quartus Prime Design Suite Update Release Notes](#)

### 1.1 High-speed Reed-Solomon IP Core v17.1

**Table 1. v17.1 November 2017**

Description	Impact
Added support for Intel® Cyclone® 10 GX devices	-
Added support for Intel Stratix® 10 devices	-

### Related Links

- [High-speed Reed-Solomon IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for High-speed Reed-Solomon IP core in the Knowledge Base](#)

### 1.2 High-speed Reed-Solomon IP Core v16.1

**Table 2. v16.1 November 2016**

Description	Impact
<ul style="list-style-type: none"> <li>• Added fracturable IP core options</li> <li>• Added two more options for the latency</li> <li>• Allowed decfail signal to be available for p&gt;36</li> <li>• Added bypass mode</li> </ul>	-

### Related Links

- [High-speed Reed-Solomon IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for High-speed Reed-Solomon IP core in the Knowledge Base](#)

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## 1.3 High-speed Reed-Solomon IP Core v16.0

**Table 3. v16.0 May 2016**

Description	Impact
<ul style="list-style-type: none"><li>Added support for multichannel decoders and backpressure.</li><li>Added support for true dual-port ROM and to refresh ROM content</li><li>Added decoder output signal options and added out_decfail signal</li></ul>	-

### Related Links

- [High-speed Reed-Solomon IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for High-speed Reed-Solomon IP core in the Knowledge Base](#)

## 1.4 High-speed Reed-Solomon IP Core v15.1

**Table 4. v15.1 September 2015**

Description	Impact
First release.	-

### Related Links

- [High-speed Reed-Solomon IP Core User Guide](#)
- [Introduction to Altera IP Cores](#)
- [Errata for High-speed Reed-Solomon IP core in the Knowledge Base](#)