



1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel FPGA IP Release Notes



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1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel® FPGA IP Release Notes

If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

Related Information

[Intel Quartus Prime Design Suite Update Release Notes](#)

1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel® FPGA IP v18.1

Table 1. Version 18.1 September 2018

Description	Impact	Notes
Added support for Intel® Cyclone® 10 GX devices.	—	—

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [Intel Cyclone 10 GX Transceiver PHY User Guide](#)

1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel FPGA IP v18.0

Table 2. v18.0 May 2018

Description	Impact	Notes
Renamed 1G/2.5G/5G/10G Multi-rate Ethernet PHY IP core to 1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel FPGA IP core as per Intel rebranding.	—	—
Added support for the following variants for Intel Stratix® 10 devices: <ul style="list-style-type: none"> 10M/100M/1G/2.5G/5G/10G (USXGMII) 10M/100M/1G/2.5G/5G/10G (USXGMII) with IEEE 1588v2 	—	—
Added support for the following variants for Intel Arria® 10 devices: <ul style="list-style-type: none"> 10M/100M/1G/2.5G/5G/10G (USXGMII) 	—	—

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel Stratix 10 FPGA IP User Guide](#)
- [Intel Arria 10 Transceiver PHY User Guide](#)

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*Other names and brands may be claimed as the property of others.



1G/2.5G/5G/10G Multi-rate Ethernet PHY IP Core v17.1

Table 3. v 17.1 November 2017

Description	Impact	Notes
Initial release in the Intel FPGA IP Library for Intel Stratix 10 devices.	—	—

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [1G/2.5G/5G/10G Multi-rate Ethernet PHY Intel Stratix 10 FPGA IP User Guide](#)

1G/2.5G/5G/10G Multi-rate Ethernet PHY v16.0

Table 4. v16.0 May 2016

Description	Impact
<ul style="list-style-type: none">• Added 1G/2.5G/5G/10G (USXGMII) operating modes for Arria 10 devices.	—

Related Information

- [Introduction to Altera IP Cores](#)
- [Arria 10 Transceiver PHY User Guide](#)

1G/2.5G/10G Multi-rate Ethernet PHY v15.1

Table 5. v15.1 November 2015

Description	Impact
Initial release. <ul style="list-style-type: none">• 2.5G, 1G/2.5G, and 1G/2.5G/10G operating modes.• Supports 1000BASE-X auto-negotiation.• Provides required latencies for the IEEE 1588v2 feature.	—

Related Information

- [Introduction to Altera IP Cores](#)
- [1G/2.5G/10G Multi-rate Ethernet PHY User Guide](#)